

**AMENDMENTS TO THE SPECIFICATION:**

Please amend the specification as follows:

[0023] In the circuit embodiment 10 shown, when a sleep or standby mode signal occurs, the scan-chain 12 is enabled by a LLE (low leakage enable) signal 28 in place of the normal TE signal. This is implemented by a simple multiplexer 30, which receives the TI signal 20 on one input and the LLE signal ~~29~~ 28 on another. The selection signal for the multiplexers 34 and 30, as well as the LLE signal ~~29~~ 28, is controlled by a finite state machine (FSM) 36.

[0029] Thus, the FSM 36 also receives (or generates) the sleep signal, and in response thereto controls the turn-on and turn-off of the ~~LLE~~ LLI signal 35 by a signal on line 26. As mentioned, since different scan chain in the design are likely to have different chain lengths the FSM 36 must ensure that each scan chain will be only active for a specific number of cycles. Accordingly, the finite state machine 36 checks the number of clock pulses against the desired number of scan flip-flops into which the vector is to be clocked, state 56. After the low leakage vector, LLI, 35 has been loaded, the LLE signals for the scan chain are turned off, state 58. In paragraph